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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 6969**

Keiichi MURAYAMA et al. : Docket No. 2003-1893A

Serial No. 10/748,158 : Group Art Unit 2814

Filed December 31, 2003 : Examiner Tuan N. Quach

HETERO-JUNCTION BIPOLAR TRANSISTOR  
AND MANUFACTURING METHOD THEREOF

THE COMMISSIONER IS AUTHORIZED  
TO CHARGE ANY DEFICIENCY IN THE  
FEE FOR THIS PAPER TO DEPOSIT  
ACCOUNT NO. 23-0975.

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement of December 27, 2004, Applicants hereby elect **Invention I**, which is drawn to a bipolar transistor. Claims 1-13 are directed to the elected invention.

Having made the required election, a full examination on the merits of the elected invention is requested.

Respectfully submitted,

Keiichi MURAYAMA et al.

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